

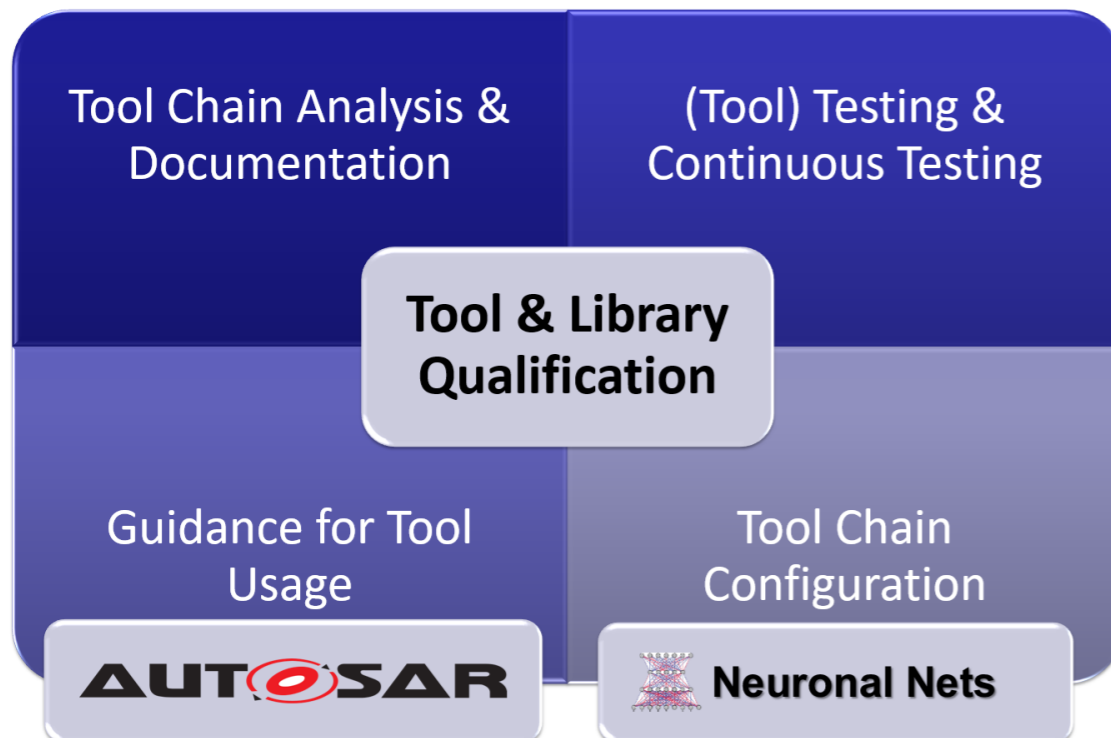
Oscar Slotosch, Validas AG

# Reference and Qualification Processes

# Validas AG About Us



- ▶ Our passion: guiding to build safe systems worldwide
- ▶ Our contribution: safe tools, safe methods and safe libraries.
- ▶ We are specialized on **tool and library qualification**



# Contents



- ▶ **Reference Process**
  - Motivation
  - Solution
  - Process Modelling Tool (PMT)
- ▶ Tool Qualification Processes
- ▶ Process Compliance
- ▶ Summary



# Motivation

- ▶ **Model & Modeling languages & tools are very powerful, e.g. UML, SYSML**
- ▶ **Modeling process has many phases**
  - Requirements Specification
  - Interface Specification
  - Design Specification
  - Implementation
- ▶ **Hard to describe processes precisely:**
  - Name: Architecture Specification
  - Input: UML-Model, . . .
  - Output UML-Model,
  - Description: Model Architecture using Class Diagrams
- ▶ **Descriptions not verifiable:**
  - Is a modeled architecture OK?
  - Is modeling language used correctly?
- ▶ **Process descriptions sometimes not useful for practical applications**

Is this clear?  
How to do this?

# Solution: Use Models



- ▶ **Use models for formalization of model-based processes**
- ▶ **Model captures**
  - Requirements, e.g. ISO 26262 (and compliances)
  - Processes
  - Models (using Meta Model)
  - Parameters
  - Tailoring
- ▶ **Provides all advantages of models for processes**
  - Precise definition
  - Static validation
  - Semantic Validation

# Goals of Process Modeling Tool



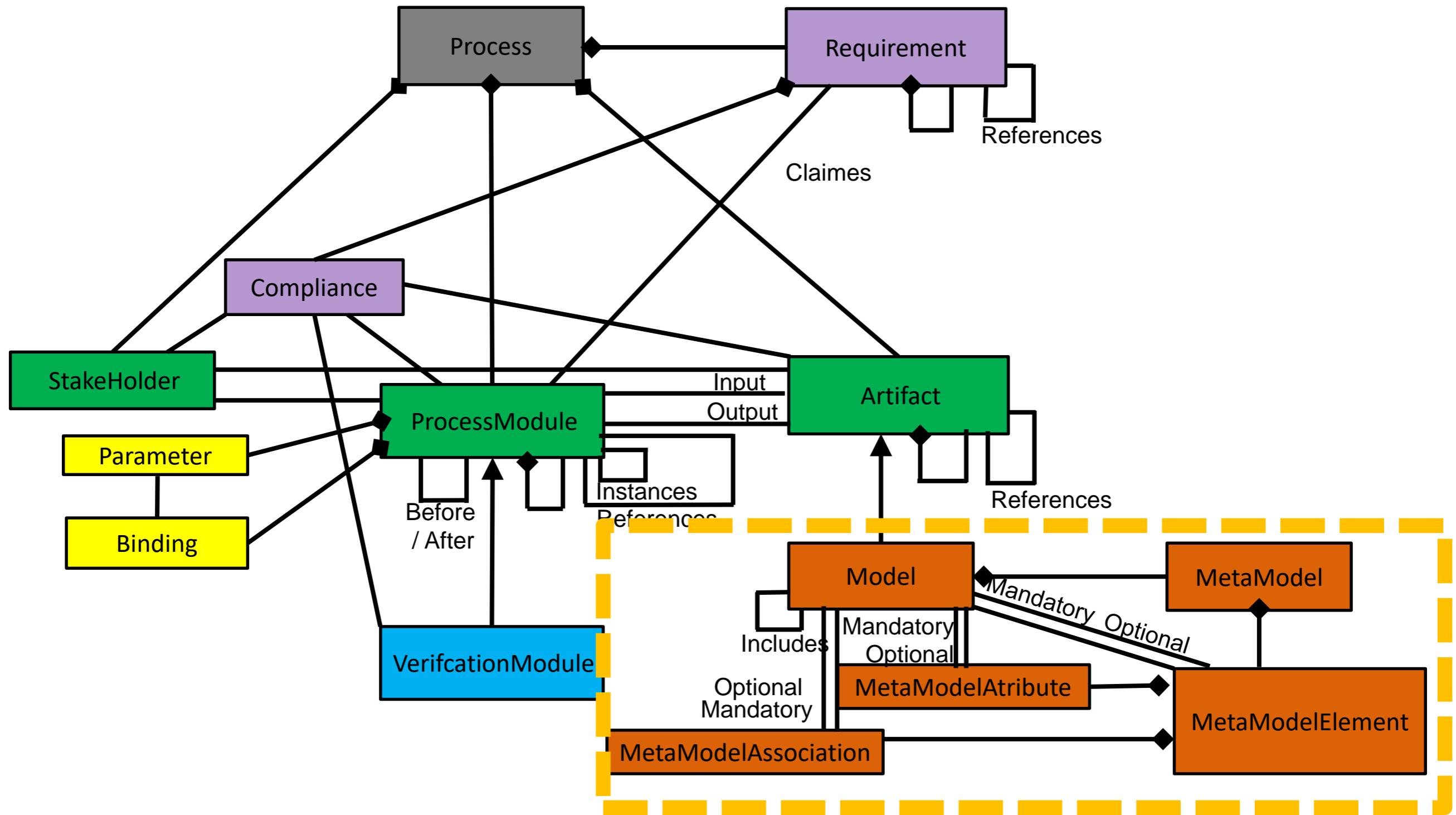
## Goals: Making Safety Easier & Safer

- ▶ **Formalize & improve processes**
- ▶ **Show compliance with safety standards (Safety Plan)**
- ▶ **Support achieving compliance (Safety Case)**
- ▶ **Document processes**

## Features:

- ▶ **Model processes (with BPMN like visualization)**
- ▶ **Validate processes (syntactically) for consistency and completeness**
- ▶ **Generate process & compliance reports**
- ▶ **Generate Verification & Validation Plans (for separate VVT)**
- ▶ **Can be used for modelling of model-based processes**
- ▶ **Has been used to certify Validas qualification processes**
- ▶ **Will be open source soon**

# Model for **Parameterized** and **Compliant Model-Based Processes**

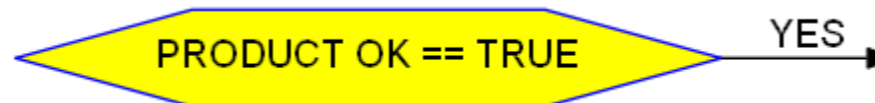
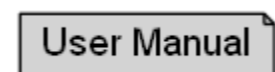




# Graphical Notation

## Elements

- ▶ **Process Module:** blue, rounded box:
- ▶ **Verification Module:** green, rounded box:
- ▶ **Hierarchical Process Module:** blue folders:
- ▶ **Hierarchical Verification Module:** green folders:
- ▶ **Artifact:** Grey box with note:
- ▶ **Model:** Orange box with note:
- ▶ **StakeHolder:** transparent box:
- ▶ **Conditions:** yellow routes:

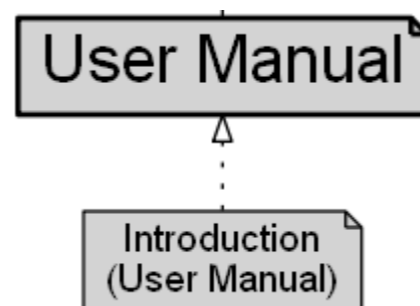


## Relations

- ▶ **Before After:** solid arrow:
- ▶ **Read/Write:** dashed arrow:
- ▶ **Artifact Containment:** dotted arrow:

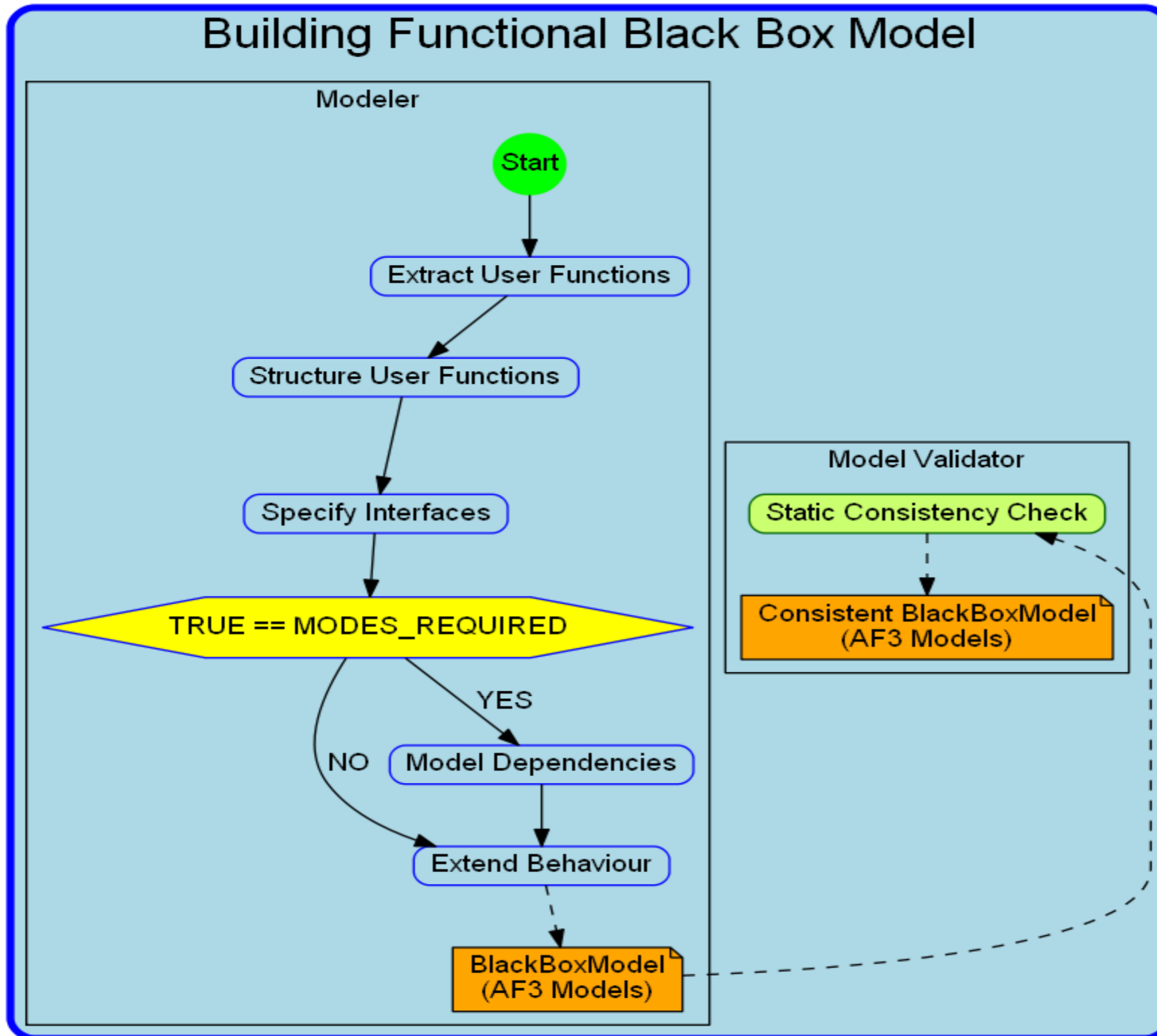
## Default

- ▶ **Start & End**





# Process Example (SPEDIT)



# Advantages



- ▶ **Technically:**
  - Precise Definition
  - Syntactic Validation (Automatically)
  - Semantic Validation: Check Lists
- ▶ **Methodically:**
  - Clarification of process & models using meta-model:
    - What is allowed?
    - What is optionally?
    - What is forbidden?
  - Precise documentation of process
  - Basis for tools support: Is this a valid architecture?
  - Formalization / Modelling triggers valuable discussions
- ▶ **Argue & document compliance with requirements, e.g. ISO 26262**

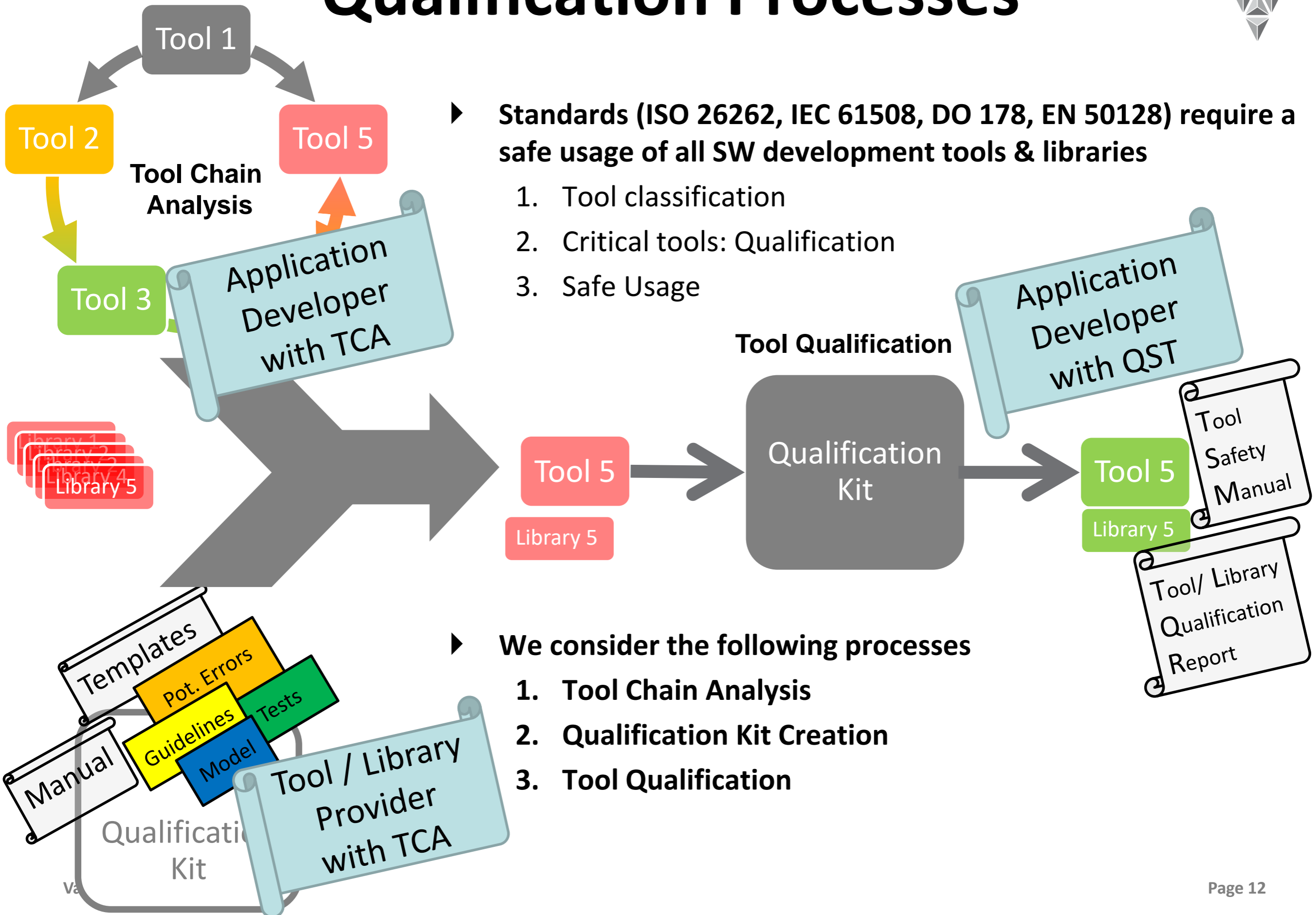
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# Qualification Processes



► Standards (ISO 26262, IEC 61508, DO 178, EN 50128) require a safe usage of all SW development tools & libraries

1. Tool classification
2. Critical tools: Qualification
3. Safe Usage

► We consider the following processes

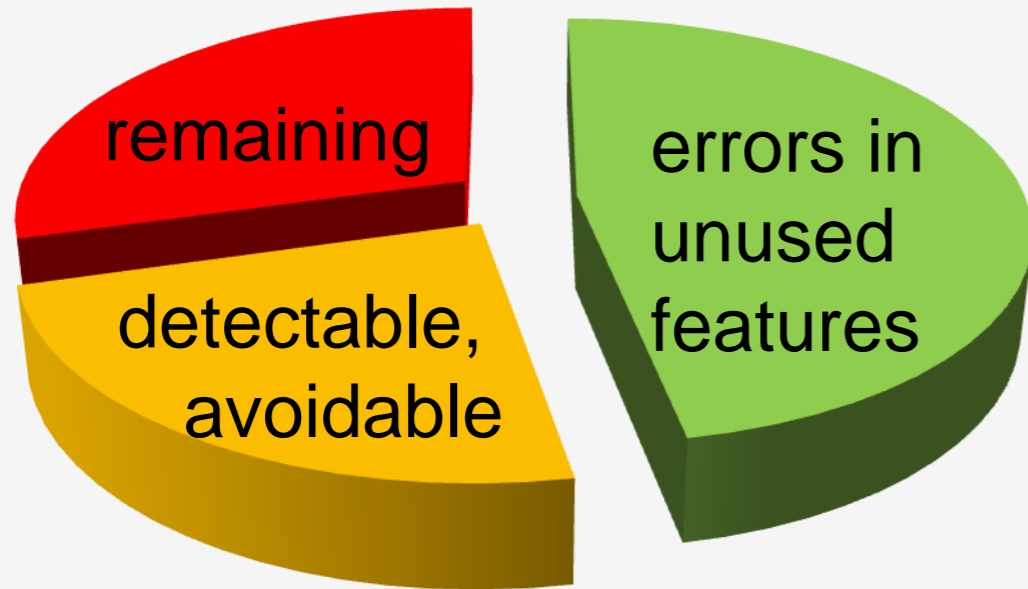
1. Tool Chain Analysis
2. Qualification Kit Creation
3. Tool Qualification

# Tool Qualification Kit Documents



## Tool Classification Report

### Potential Tool Errors



## Tool Safety Manual

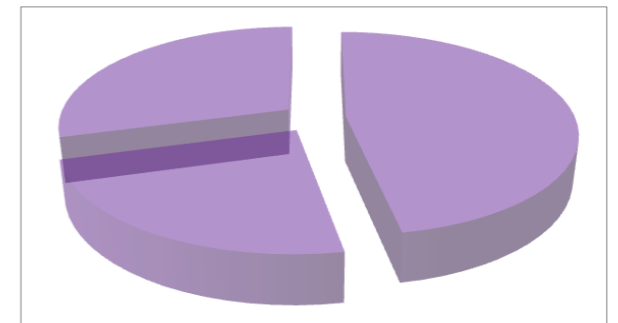
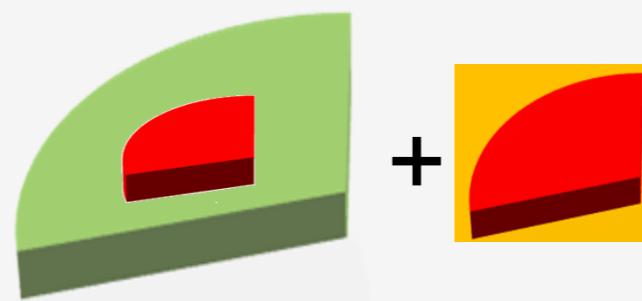
### Error Mitigations



## Tool Qualification Plan



## Tool Qualification Report



Known Errors

QKit **U**ser **G**uide

QKit **V**erification and **V**alidation **P**lan

QKit **D**eveloper **G**uide

QKit **V**erification and **V**alidation **R**eport

# SPEDIT-Qualification Kits (“Growing”)



- ▶ **Growing QKit: partial, can be extended**
- ▶ **Compliant with ISO 26262, IEC 61508, DO-178, EN 50128,...**
- ▶ **Build according to Validas TÜV-certified compliance process**
- ▶ **AutoFocus-QKit**
  - Qualifies Schedule generation feature of AutoFOCUS
  - First QKit for a formal methods tool
  - Based on interface & redundancy
  - Demonstration -> Exhibition (Validas)
- ▶ **PTC-Modeller-QKit**
  - Qualifies Basic DataBase Functionality (“No Side-Effects”!!)
  - “Mitigates” other features
  - Demonstration -> Validas

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# Motivation: (QKit-)Compliances

Product - ASIL	ASIL D
Item	<ul style="list-style-type: none"> <li>3. Concept phases</li> <li>4.8 Concept safety management</li> <li>4.9 Non destruction</li> <li>4.10 Initiation of the safety lifecycle</li> <li>4.11 Hazard analysis and risk management</li> <li>4.12 Functional safety concepts</li> </ul>
SEooC	
Unchanged SWC („Library“)	
Tool	<ul style="list-style-type: none"> <li>4.13 Software safety and functional programming</li> <li>4.14 Configuration management</li> <li>4.15 Change management</li> <li>4.16 Verification</li> <li>4.17 Implementation &amp; Deployment</li> <li>4.18 Software for consideration of maintenance</li> </ul>



## SWC Qualification Kit: (6-SEOOOC Compliant)

- Function-Specifications
- Potential Errors
- Known Bugs
- Code-Coverage (ALL ASIL)
- **Architecture**
- **Programming Guidelines**
- **Tool Qualification Reports**
- **Many Tests / TAU**
- ...

## SWC Qualification Kit: (8-12 Compliant)

- Function-Specifications
- Potential Errors
- Known Bugs
- **Code-Coverage (ASIL D)**
- **negative Tests / TAU**
- ...

## Tool Qualification Kit: (8-11 Compliant)

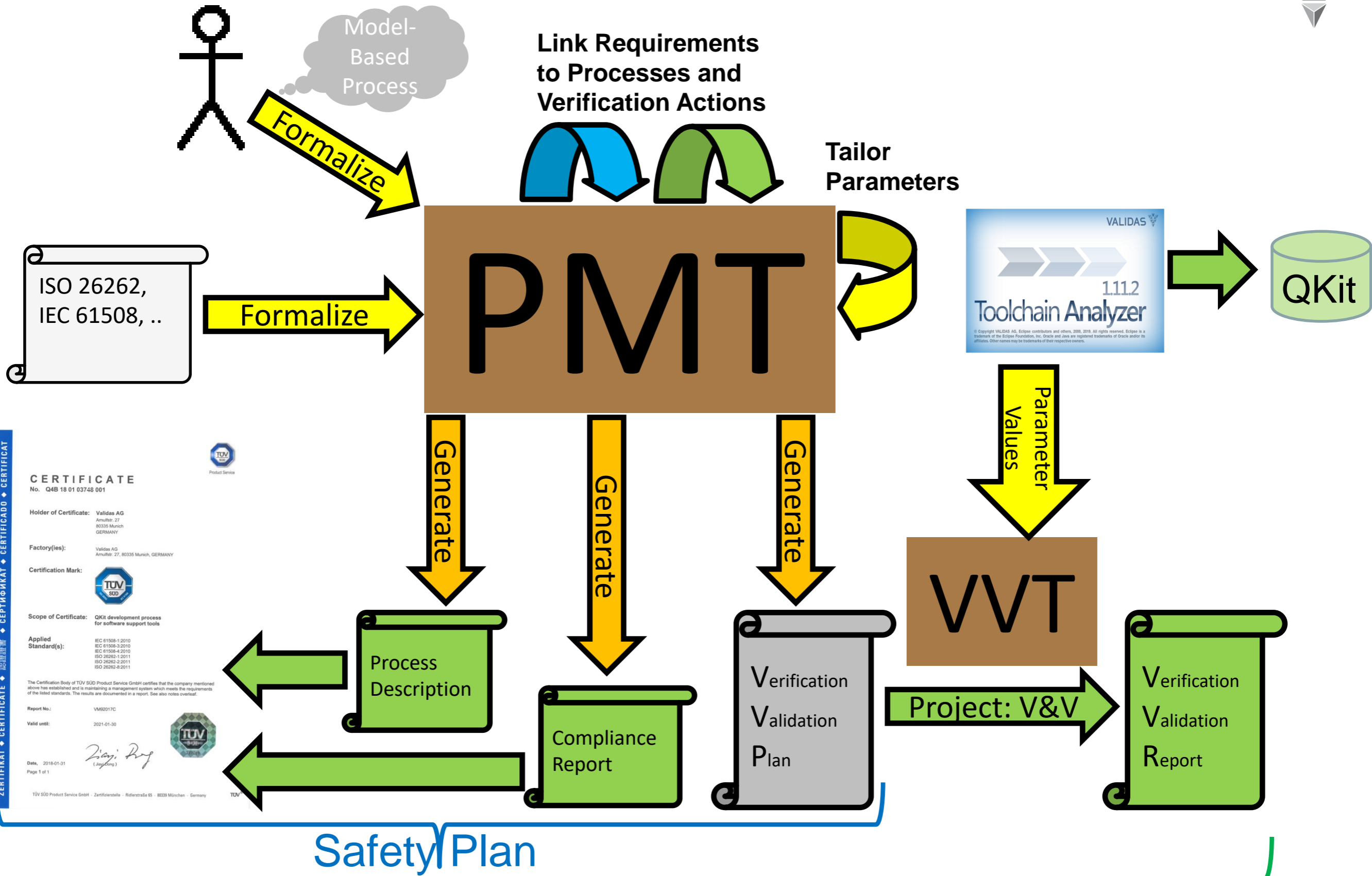
- Feature-Specifications
- Potential Errors
- Known Bugs
- **Mitigations**
- Requirements Tests / TAU
- ...



similar structures:  
development processes



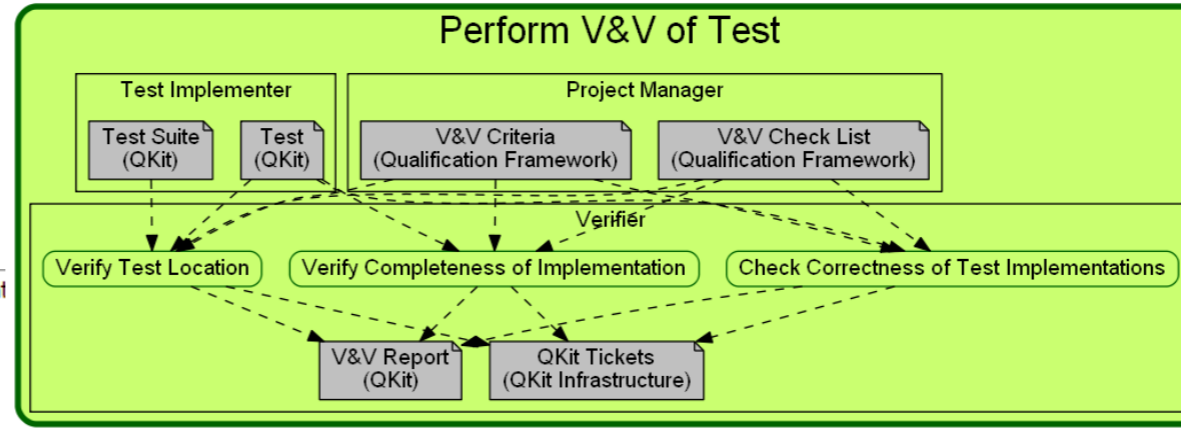
# Process Modeling Tool PMT



# Example Compliance Argumentation

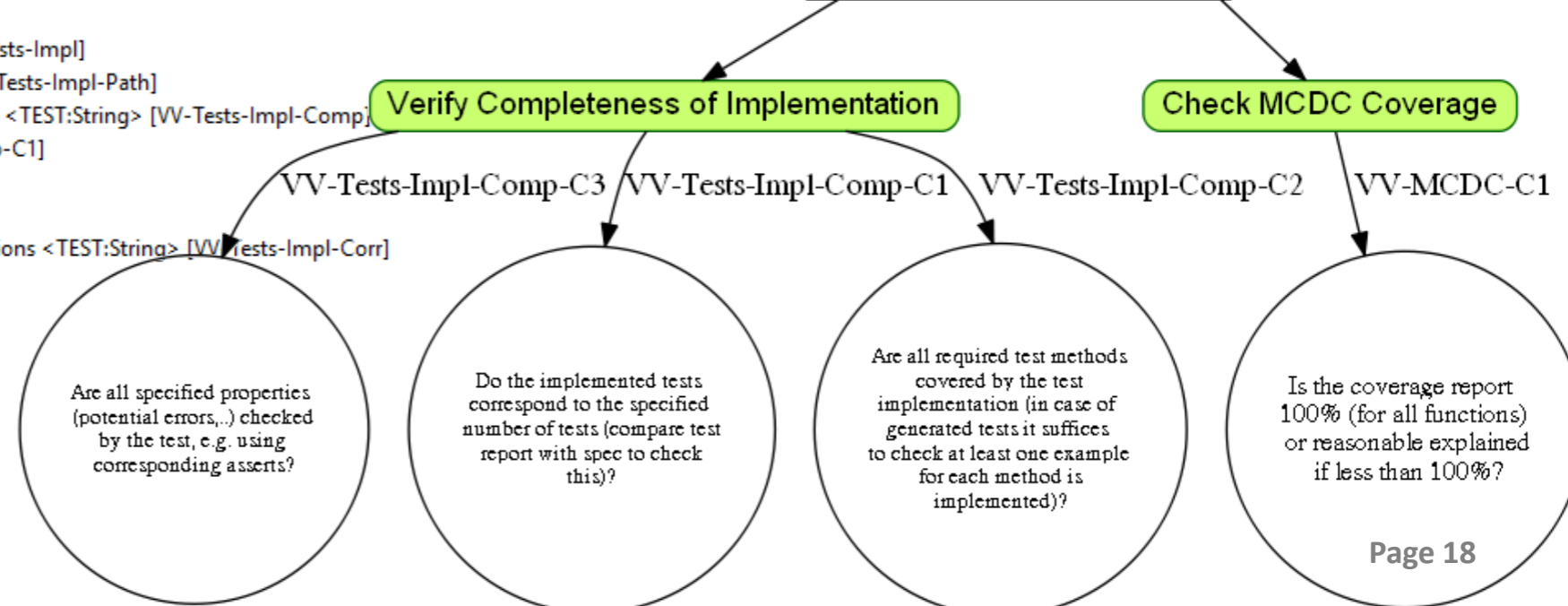
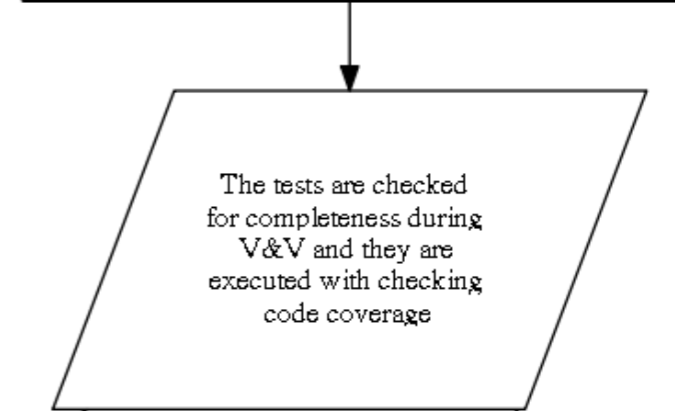


- ▶ We use Test Generators that have to generate complete test case
- ▶ We check this during V&V



- file:/E:/svn/ISO26262\_TQ/trunk/Certification/PMT/ToolQualification/ToolAndLibQual.pmt
- Process Validas Qualification Processes
  - Requirement ISO ISO 26262
  - Requirement EN EN 50128
  - Requirement IEC IEC 61508
  - Requirement Generator Requirements
    - OR Term ((USE\_TEST\_GENERATOR == TRUE) || (USE\_SPEC\_GENERATOR == TRUE))
    - Requirement Generator Correctness
    - Requirement Generator Completeness
      - Requirement Test Generator Completeness
      - Requirement Specification Generator Completeness
      - Compliance Generator Completeness Compliance
      - Compliance Generator Requirements Compliance
    - Requirement Lib-QKit-Main Library QKit Requirements
  - Process Module Validas Process Library
    - Process Module Preparation Phase
    - Process Module Development Phase
    - Process Module Validation Phase
      - Verification Module VT1: Perform V&V [VV-VT1]
        - Verification Module Verify Product [VV-Prod]
          - Verification Module Perform V&V of Model [VV-Model]
          - Verification Module Perform V&V of Test <TEST:String> [VV-Tests-Impl]
            - Verification Module Verify Test Location <TEST:String> [VV-Tests-Impl-Path]
            - Verification Module Verify Completeness of Implementation <TEST:String> [VV-Tests-Impl-Comp]
              - Criterion Test Numbers Complete? [VV-Tests-Impl-Comp-C1]
              - Criterion Test Methods [VV-Tests-Impl-Comp-C2]
              - Criterion Properties Verified [VV-Tests-Impl-Comp-C3]
            - Verification Module Check Correctness of Test Implementations <TEST:String> [VV-Tests-Impl-Corr]
            - Verification Module Verify Documentation [VV-Doc]
            - Verification Module V&V TAU [VV-TAU]
            - Verification Module Validate Output [VV-Out]
            - Verification Module Validate Process [VV-Proc]
          - Process Module VT0: Manage V&V
          - Process Module Release QKit

The generated test cases shall be complete



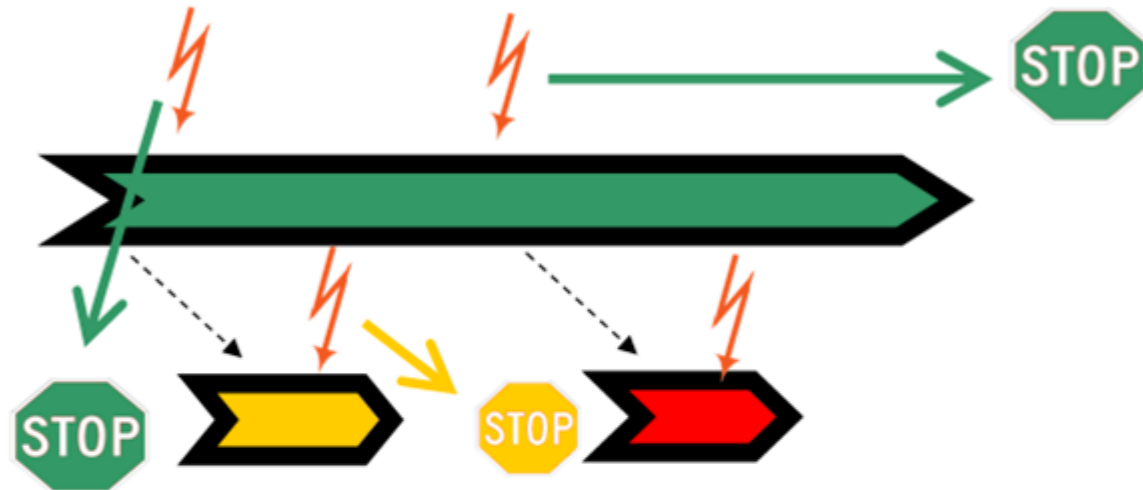
# Generated Compliance Report



Compliance Report

for

AutoFOCUS3 Modeling Process



## 1 Scope of this Document

### 2 Parameters of Process AutoFOCUS3 Modeling Process

#### 2.1 Types in Process AutoFOCUS3 Modeling Process

#### 2.2 Process Parameters of Process AutoFOCUS3 Modeling Process

#### 2.3 Planning Parameters of Process AutoFOCUS3 Modeling Process

#### 2.4 Project Parameters of Process AutoFOCUS3 Modeling Process

### 3 Main Requirements (Claims) for Process AutoFOCUS3 Modeling Process

#### 3.1 Requirement Specification Quality

### 4 Requirements for Process AutoFOCUS3 Modeling Process

#### 4.1 Requirement Specification Quality

## 5 V&V Checks for AutoFOCUS3 Modeling Process

### 6 Compliance for Process AutoFOCUS3 Modeling Process

#### 6.1 Compliance with Specification Quality

Version:	Template 0.2 / Document 0.3
Date:	2018-10-30
Status:	Generic / <b>Generated</b> / Reviewed / Final
Author:	Dr. Oscar Slotosch
File:	CR.docm
Size:	11 Pages

### 3.1 Requirement Specification Quality

This section describes requirement Specification Quality.

#### VerificationModule: Check Model Consistency

##### Name:

Check Model Consistency

##### Description:

The consistency of the model is statically checked using the tools. Consistency checks of black box models automatically detect

- empty interfaces
- empty descriptions
- undefined types
- non-matching types

The result of the consistency check is a consistency report. If the model is successfully checked, the result is "successfully checked".

##### Relevant Parameter:

- FUNCTION, see Table 7

##### Owner:

Model Validator

##### Input Artefacts:

- BlackBoxModel, see

##### Output Artefacts:

- Validation Report, see

##### Verifies:

- Model: BlackBoxModel, see
- ProcessModule: Building Functional Black Box Model, see

#### Compliance Black Box Model Compliance

##### Name:

Black Box Model Compliance

##### Description:

By creating functional black box models the specification quality is increased. One obvious benefit of the models is that they can be automatically validated, i.e. if Validation has been performed the quality increased.

##### Requirement:

- Specification Quality, see Table 9

##### Implementing Process Module:

- Building Functional Black Box Model

##### V&V Check:

- Check Model Consistency, see Table 11

Table 12 Compliance Black Box Model Compliance

Table 11 VerificationModule: Check Model Consistency

Table 9 Requirement: Specification Quality

#### Requirement: Specification Quality

##### Name:

Specification Quality

##### Description:

Consistent documentation, i.e., Auto-generation of documentation.

##### Recommended From:

ASIL\_A

##### Recommended To:

ASIL\_D

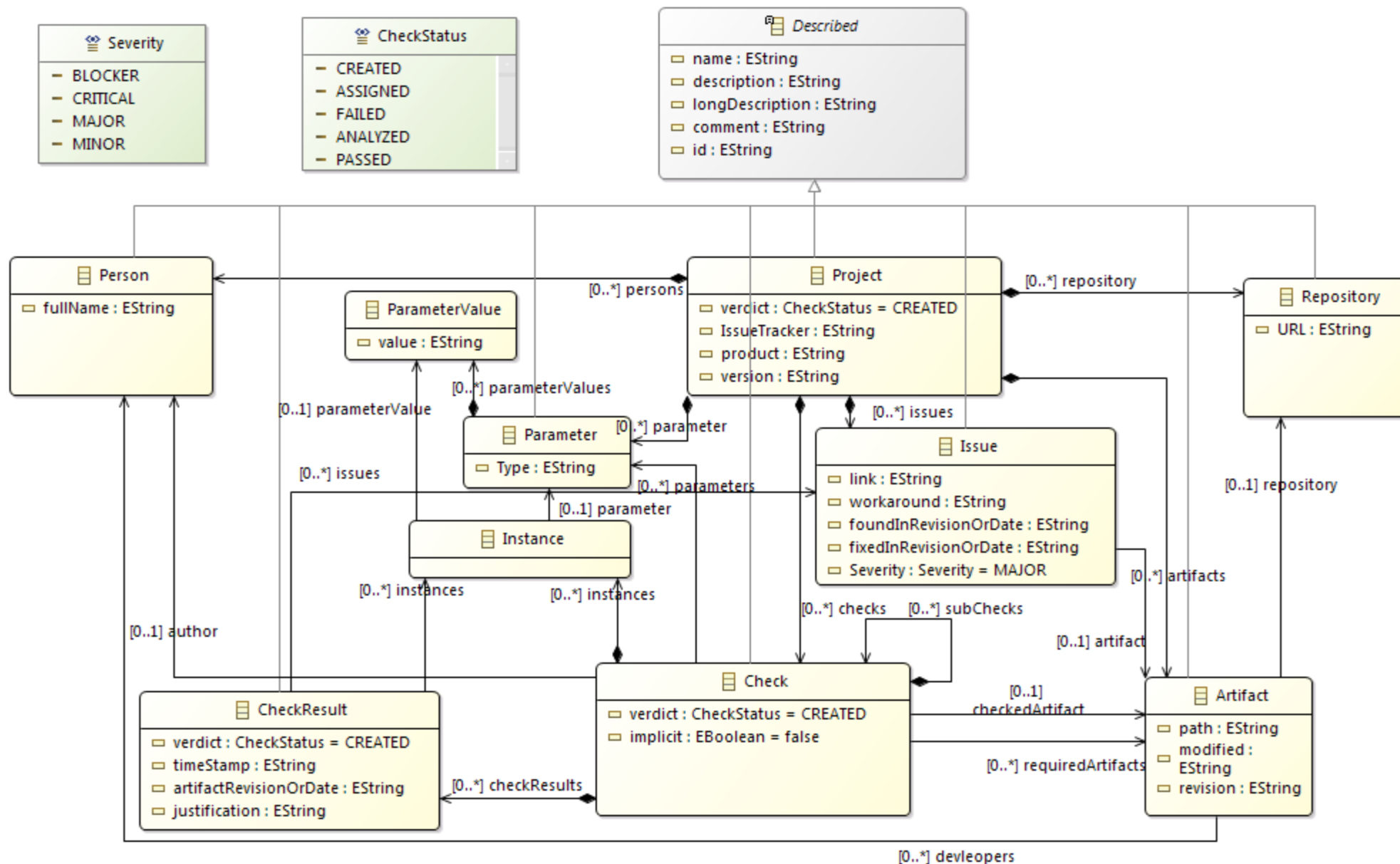
##### Implementing Process (Satisfying this claim):

- Building Functional Black Box Model



# Verification and Validation Model

- ▶ V&V Model documents Verification and Validation (for all instances of the parameters)
- ▶ V&V Model is basis for Verification and Validation Tool (VVT)
- ▶ VVT Model is generated from PMT (based on Process and Planning parameters)
- ▶ Meta Model of VVT:



# Example: Corresponding Checklist



	A	B	C	D	E	N	O	P	Q
1 Parameters		Result: Combinations Considered in Test Strategy	Date: Combinations Considered in Test Strategy	Issues: Combinations Considered in Test Strategy		Explain: Combinations Considered in Test Strategy	Result: NaNOK=0	Date: NaNOK=0	Issues: NaNOK=0
155	TEST=test__aeabi_fsub	PASS	17/02/2019	Yes, 6 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 66 tests of __aeabi_fsub have been analyzed completely during		
156	TEST=test__aeabi_idiv	PASS	17/02/2019	Yes, 6 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 55 tests of __aeabi_idiv have been analyzed completely during		
157	TEST=test__aeabi_idiv0	PASS	17/02/2019	Yes, 4 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 36 tests of __aeabi_idiv0 have been analyzed completely during		
158	TEST=test__aeabi_ldiv0	PASS	17/02/2019	Yes, 4 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 36 tests of __aeabi_ldiv0 have been analyzed completely during		
159	TEST=test__aeabi_lmul	PASS	17/02/2019	Yes, 6 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 52 tests of __aeabi_lmul have been analyzed completely during		
160	TEST=test__aeabi_uidiv	PASS	17/02/2019	Yes, 6 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 55 tests of __aeabi_uidiv have been analyzed completely during		
161	TEST=test_abs	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	#316 Yes, all 34 tests of abs have been analyzed completely during		
162	TEST=test_acos	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 55 tests of acos have been analyzed completely during		
163	TEST=test_acosf	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 55 tests of acosf have been analyzed completely during		
164	TEST=test_asin	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 52 tests of asin have been analyzed completely during		
165	TEST=test_asinf	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 52 tests of asinf have been analyzed completely during		
166	TEST=test_atan	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 55 tests of atan have been analyzed completely during		
167	TEST=test_atan2	PASS	17/02/2019	Yes, 4 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 58 tests of atan2 have been analyzed completely during		
168	TEST=test_atan2f	PASS	17/02/2019	Yes, 4 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 59 tests of atan2f have been analyzed completely during		
169	TEST=test_atanf	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 56 tests of atanf have been analyzed completely during		
170	TEST=test_ceil	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 52 tests of ceil have been analyzed completely during		
171	TEST=test_ceilf	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 52 tests of ceilf have been analyzed completely during		
172	TEST=test_cos	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 53 tests of cos have been analyzed completely during		
173	TEST=test_cosf	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 53 tests of cosf have been analyzed completely during		
174	TEST=test_cosh	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 51 tests of cosh have been analyzed completely during		
175	TEST=test_coshf	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 51 tests of coshf have been analyzed completely during		
176	TEST=test_exp	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 51 tests of exp have been analyzed completely during		
177	TEST=test_expf	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 54 tests of expf have been analyzed completely during		
178	TEST=test_fabs	PASS	17/02/2019	Yes, 2 combinations with other functions have been specified & tested (terms)	PASS	17/02/2019	Yes, all 54 tests of fabs have been analyzed completely during		

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# Summary



- ▶ **SPEDIT-Reference process**
  - Is generic & model-based
  - Can be adapted to any project & company using PMT
  - Can be validated (syntactically & semantically)
  - Can be safe & compliant with ISO 26262, IEC 61508,..
- ▶ **PMT Prototype**
  - Is freely available from <http://www.validas.de/tools/>
- ▶ **VVT Prototype**
  - Is freely available from <http://www.validas.de/tools/>
- ▶ **Approach has been applied to certify Validas tool qualification process**

